



RIK0127A - High Speed Digital Scan Module

Revised: 01/07/2009 - 04/06/2009

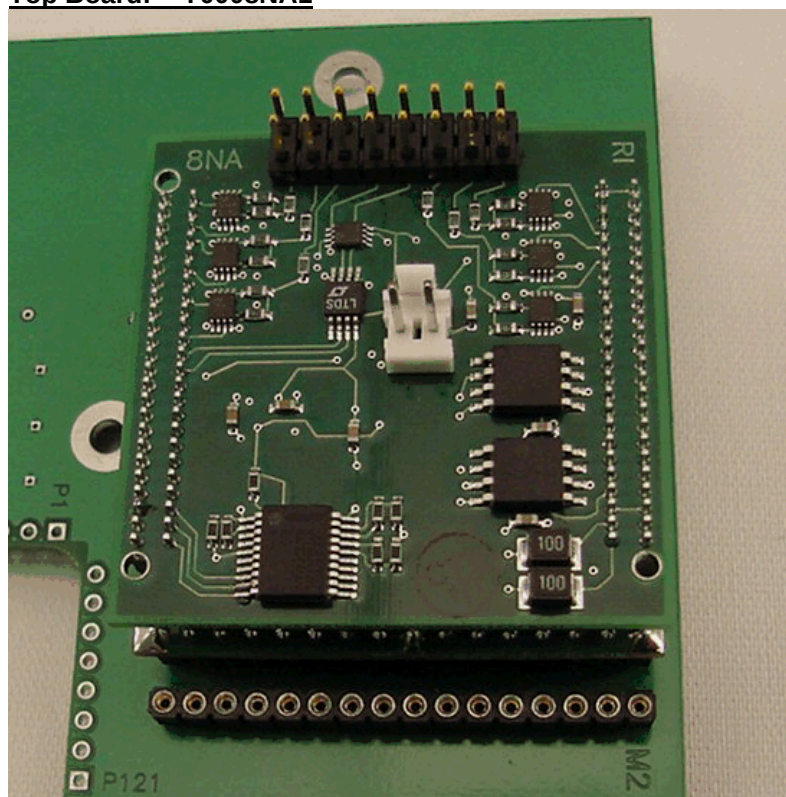
Topic(s): Fixture

Doc ID:DFES-7N3PJZ

Improved physical interfaces for easier integration than RIK0017A. Adds Scan and 16 MegaVector memory depth.

Provides High Speed Digital signal to the DUT from the fixture.

Top Board: = Y0008NA1



High Speed Scan Module Kit (1 Module)

2 Sets of 4 I/O Pins: DB 1 to 4 and DB 5 to 8
 Von and Voff Set Range: 0 to 4V
 Vcompare: 0 to 4V
 Serial Control Supports: Data Read, Data Write, Clock, Strobe
 Serial Write: Up to 80 Mbits/sec.
 Scan Support: Yes
 Parametric Measure: Yes

Y0008NA1 HS Digital Scan Pin Out

Pin#	Function	Note:	Connector Type
H5-1	Configurable	Data, Clock, Strobe, or Static	Header Pin
H6-2	Configurable	Data, Clock, Strobe,	Header Pin

		or Static	
H7-3	Configurable	Data, Clock, Strobe, or Static	Header Pin
H8-4	Read A		Header Pin
H9-5	Read B		Header Pin
H10-6	Configurable	Data, Clock, Strobe, or Static	Header Pin
H11-7	Configurable	Data, Clock, Strobe, or Static	Header Pin
H12-8	Configurable	Data, Clock, Strobe, or Static	Header Pin
H5-12 P2	GND		
H4-1	GND		Header Pin
H4-2	MUX I/O		Header Pin

Configuration Window Settings:

Module = Y2000H1A

Inst#: RI74315C

Program = Y18NA11A

Vlo = 0

Vneg = 2

Vector Freq = 80000000

Vhi = 0

Vcmp = 4

Clock Freq = 20000000

DB1 = off

Scan Location B = blank

Scan Location = blank

NOTE: Module will only work in M1 or M5 positions of the carrier board.

Module Browser

Pool Node Modules Help

Y00013C1	Ri7240C	Testhead	M1	Y2000H1A
Y00020B1	Ri7420D	DutCont		
Y00013C1	Ri74210A	Ri7420		
Y00022B1	Ri7430B	Ri7431		
Y00065A1	Ri74315C			
Y0000VD1	Ri7322J	Receiver		
Y0002EA1	Ri7710G	RecLo		

DB1 off			ScanLocationB 
Vlo 0.0			FailCountB 
Vneg 0.0	Program Y18NA11A		FailCountA 
Vector Frequency 20000000.0	Vcomp 0.0	Location M1	ScanLocation 
Vhi 0.0	Clock Frequency 20000000.0	Name M1	LogicAnaA 
			LogicAnaB 

Configuring the Carrier:

RIK0127A_HSDigConfiguration_RI7100.pdf



RIK0127A_HSDigConfiguration_RI7100.odp

Scan Requirements:

tbd.