

Sheet1

**Launch 1**

	1	2	3	4	
1	High – I VSS (A3)	High – I VSS (A4)	High – VDD (B3)	High – VDD (B4)	4
5	High – I VSS (A5)	High – I VSS (A6)	High – VDD (B5)	High – VDD (B6)	8
9	High – I VSS (A7)	High – I VSS (A8)	High – VDD (B7)	High – VDD (B8)	12
13	High – I VSS (A9)	High – I VSS (A10)	High – VDD (B9)	High – VDD (B10)	16

**Launch 2**

	1	2	3	4	
1	HV1_Out (C1)	HV1_RTN (C2)	HV4_Out (D6)	HV4_RTN (D7)	4
5	INTRLK (HV1) (C4)	INTRLK (HV1) (C5)	INTRLK (HV4) (D9)	INTRLK (HV4) (D10)	8
9	RLY1 (I2)	RLY2 (I3)	RLY3 (I4)	RLY4 (I5)	12
13			Aux (B1)	GND (I1)	16

**Launch 3**

	1	2	3	4	
1	HV2_Out (C6)	HV2_RTN (C7)	HV3_Out (D1)	HV3_RTN (D2)	4
5	INTRLK (HV2) (C9)	INTRLK (HV2) (C10)	INTRLK (HV3) (D4)	INTRLK (HV3) (D5)	8
9	RLY5 (I6)	RLY6 (I7)	RLY7 (I8)	RLY8 (I9)	12
13	GRES (A1)	Spare (A2)	Aux (B2)	+12V RLY (I10)	16